NPTEL Video Course - Electronics and Communication Engineering - Digital Computer Organization

Subject Co-ordinator - Prof. P.K. Biswas

Co-ordinating Institute - IIT - Kharagpur

Sub-Titles - Available / Unavailable  |  MP3 Audio Lectures - Available / Unavailable


Lecture 1 - Introduction to Digital Computer Organization
Lecture 2 - CPU Design - I
Lecture 3 - CPU Design - II
Lecture 4 - CPU Design Tirning and Control
Lecture 5 - Micro programmed Control - I
Lecture 6 - Micro programmed Control - II
Lecture 7 - Pipeline Concept - I
Lecture 8 - Pipeline Concept - II
Lecture 9 - Pipeline Concept - III
Lecture 10 - Pipeline CPU - I
Lecture 11 - Pipeline CPU - II
Lecture 12 - Pipeline CPU - III
Lecture 13 - Memory Organization - I
Lecture 14 - Memory Organization - II
Lecture 15 - Memory Organization - III
Lecture 16 - Memory Organization - IV
Lecture 17 - Memory Organization - V
Lecture 18 - Cache Memory Architecture
Lecture 19 - Cache Memory Architecture RAM Architecture
Lecture 20 - RAM Architecture
Lecture 21 - DAM Architecture-1
Lecture 22 - DAM Architecture Buffer Cache
Lecture 23 - Buffer Cache
Lecture 24 - Secondary Storage Organization - I
Lecture 25 - Secondary Storage Organization - II
Lecture 26 - Secondary Storage Organization - III
Lecture 27 - I/O Subsystem Organization
Lecture 28 - Error Detection and Correction

-------------------------------------------------------------------------------------------------
Get Digi-MAT (Digital Media Access Terminal) For High-Speed Video Streaming of NPTEL and Educational Video Courses in LAN

www.digimat.in