NPTEL Video Course - Computer Science and Engineering - NOC:Hardware Modeling using Verilog

Subject Co-ordinator - Prof. Indranil Sengupta

Co-ordinating Institute - IIT - Kharagpur

Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable

Lecture 30 - Modeling Memory
Lecture 31 - Modeling Register Banks
Lecture 32 - Basic Pipelining Concepts
Lecture 33 - Pipeline Modeling - Part 1
Lecture 34 - Pipeline Modeling - Part 2
Lecture 35 - Switch Level Modeling - Part 1
Lecture 36 - Switch Level Modeling - Part 2
Lecture 37 - Pipeline Implementation Of A Processor - Part 1
Lecture 38 - Pipeline Implementation Of A Processor - Part 2
Lecture 39 - Pipeline Implementation Of A Processor - Part 3
Lecture 40 - Verilog Modeling Of The Processor - Part 1
Lecture 41 - Verilog Modeling Of The Processor - Part 2