

NPTEL Video Lecture Topic List - Created by LinuXpert Systems, Chennai

NPTEL Video Course - Computer Science and Engineering - Low Power VLSI Circuits and Systems

Subject Co-ordinator - Prof. Ajit Pal

Co-ordinating Institute - IIT - Kharagpur

Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable

- Lecture 1 - Introduction & Course Outline
- Lecture 2 - MOS Transistors - I
- Lecture 3 - MOS Transistors - II
- Lecture 4 - MOS Transistors - III
- Lecture 5 - MOS Transistors - IV
- Lecture 6 - MOS Inverters - I
- Lecture 7 - MOS Inverters - II
- Lecture 8 - MOS Inverters - III
- Lecture 9 - MOS Inverters - IV
- Lecture 10 - Static CMOS Circuits - I
- Lecture 11 - Static CMOS Circuits - II
- Lecture 12 - MOS Dynamic Circuits - I
- Lecture 13 - MOS Dynamic Circuits - II
- Lecture 14 - Pass Transistor Logic Circuits - I
- Lecture 15 - Pass Transistor Logic Circuits - II
- Lecture 16 - MOS Memories
- Lecture 17 - Finite State Machines
- Lecture 18 - Switching Power Dissipation
- Lecture 19 - Tutorial - I
- Lecture 20 - Dynamic Power Dissipation
- Lecture 21 - Leakage Power Dissipation
- Lecture 22 - Supply Voltage Scaling - I
- Lecture 23 - Supply Voltage Scaling - II
- Lecture 24 - Supply Voltage Scaling - III
- Lecture 25 - Supply Voltage Scaling - IV
- Lecture 26 - Tutorial - II
- Lecture 27 - Minimizing Switched Capacitance - I
- Lecture 28 - Minimizing Switched Capacitance - II
- Lecture 29 - Minimizing Switched Capacitance - III

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- Lecture 30 - Minimizing Switched Capacitance - IV
- Lecture 31 - Minimizing Switched Capacitance - V
- Lecture 32 - Minimizing Leakage Power - I
- Lecture 33 - Minimizing Leakage Power - II
- Lecture 34 - Minimizing Leakage Power - III
- Lecture 35 - Variation Tolerant Design
- Lecture 36 - Adiabatic Logic Circuits
- Lecture 37 - Battery-Driven System Design
- Lecture 38 - CAD Tools for Low Power
- Lecture 39 - Tutorial - III
- Lecture 40 - Course Summary