NPTEL Video Lecture Topic List - Created by LinuXpert Systems, Chennai

```
NPTEL Video Course - Computer Science and Engineering - Computer Architecture (Dr. Mainak Chaudhuri)
Subject Co-ordinator - Dr. Mainak Chaudhuri
Co-ordinating Institute - IIT - Kanpur
Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable
Lecture 1 - Introduction, Amdahl's law, CPI equation
Lecture 2 - CPI equation, research practices, instruction set architecture
Lecture 3 - Instruction set architecture
Lecture 4 - Instruction set architecture
Lecture 5 - Instruction set architecture, case study with MIPS-I
Lecture 6 - Case study with MIPS-I
Lecture 7 - Case study with MIPS-I
Lecture 8 - Binary instrumentation for architectural studies
Lecture 9 - Binary instrumentation for architectural studies
Lecture 10 - Basic pipelining, branch prediction
Lecture 11 - Basic pipelining, branch prediction
Lecture 12 - Basic pipelining, branch prediction
Lecture 13 - Basic pipelining, branch prediction
Lecture 14 - Basic pipelining, branch prediction
Lecture 15 - Basic pipelining, branch prediction
Lecture 16 - Basic pipelining, branch prediction
Lecture 17 - Basic pipelining, branch prediction
Lecture 18 - Basic pipelining, branch prediction
Lecture 19 - Basic pipelining, branch prediction
Lecture 20 - Dynamic scheduling, speculative execution
Lecture 21 - Dynamic scheduling, speculative execution
Lecture 22 - Dynamic scheduling, speculative execution
Lecture 23 - Dynamic scheduling, speculative execution
Lecture 24 - Dynamic scheduling, speculative execution
Lecture 25 - Virtual memory and caches
Lecture 26 - Virtual memory and caches
Lecture 27 - Virtual memory and caches
Lecture 28 - Topics in memory system, DRAM and SRAM technology
Lecture 29 - Topics in memory system, DRAM and SRAM technology
```

NPTEL Video Lecture Topic List - Created by LinuXpert Systems, Chennai

```
Lecture 30 - Topics in memory system, DRAM and SRAM technology
Lecture 31 - Case study
Lecture 32 - Case study
Lecture 33 - Case study
Lecture 34 - Case study
Lecture 35 - Input/Output
Lecture 36 - Simultaneous multithreading, multi-cores
```

Cat Digi MAT (Digital Madia Access Tarminal) For High Speed Video Strooming of NDTEL and Educational Video Courses in LAN